

**Amendments to the Claims:**

This listing of Claims will replace all prior versions, and listings, of Claims in the application:

5   **Listing of Claims:**

Claim 1 (previously presented): A liquid crystal display panel comprising:

a first substrate;

a second substrate having an active region;

a sealant positioned on the second substrate and surrounding the active region for

10       adhering the second substrate to the first substrate;

a spacer wall positioned on the second substrate and between the sealant and the active region, the spacer wall having at least one liquid crystal injected opening and at least one spacer block positioned in the liquid crystal injected opening; and

15       a liquid crystal layer positioned between the first substrate, the second substrate, and the sealant;

wherein the spacer wall supports the first substrate and prevents the liquid crystal layer from being contaminated by the sealant, and the spacer block prevents the sealant from contaminating the liquid crystal layer.

20   Claim 2 (previously presented): The liquid crystal display panel of claim 1 wherein the second substrate further comprises a peripheral region surrounding the active region and a thin film layer patterned corresponding to the peripheral region and positioned under the spacer wall, wherein both the sealant and the spacer wall are located on the thin film layer.

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Claim 3 (original): The liquid crystal display panel of claim 2 wherein the thin film layer is an anti-reflective layer.

Claim 4 (original): The liquid crystal display panel of claim 2 wherein the thin film layer is a first alignment layer.

5 Claim 5 (previously presented): The liquid crystal display panel of claim 4 further comprising:

a second alignment layer positioned on the first substrate and opposite to the first alignment layer and patterned corresponding to the first alignment layer, wherein the first alignment layer and the second alignment layer are both vertical alignment (VA) layers.

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Claim 6 (previously presented): A liquid crystal display panel comprising:

a first substrate;

a second substrate having an active region, a peripheral region surrounding the active region, and a thin film layer patterned corresponding to the peripheral region;

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a sealant positioned on thin film layer of the second substrate and surrounding the active region for adhering the second substrate to the first substrate;

a spacer wall positioned on the thin film layer of the second substrate and between the sealant and the active region; and

a liquid crystal layer positioned between the first substrate, the second substrate, and the sealant;

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wherein the spacer wall supports the first substrate and prevents the liquid crystal layer from being contaminated by the sealant.

25 Claim 7 (original): The liquid crystal display panel of claim 6 wherein the thin film layer is an anti-reflective layer.

Claim 8 (original): The liquid crystal display panel of claim 6 wherein the thin film layer

is a first alignment layer.

Claim 9 (original): The liquid crystal display panel of claim 8 further comprising:  
a second alignment layer positioned on the first substrate and opposite to the first  
5 alignment layer, wherein the first alignment layer and the second alignment layer are both  
vertical alignment layers.

Claim 10 (original): The liquid crystal display panel of claim 6 wherein the spacer wall  
comprises at least one liquid crystal injected opening for allowing liquid crystal  
10 molecules to be injected into space between the first substrate and the second substrate.

Claims 11-14 (canceled)

Claim 15 (previously presented): A liquid crystal on silicon (LCOS) display panel  
15 comprising:  
a first substrate;  
a second substrate having an active region;  
a sealant positioned on the second substrate and surrounding the active region for  
adhering the second substrate to the first substrate;  
20 a spacer wall positioned on the second substrate and between the sealant and the active  
region for enclosing the active region; and  
a liquid crystal layer positioned between the first substrate, the second substrate, and the  
sealant;  
wherein the spacer wall supports the first substrate and prevents the liquid crystal layer  
25 from being contaminated by the sealant.

Claim 16 (previously presented): The liquid crystal on silicon display panel of claim 15  
wherein the second substrate further comprises a peripheral region surrounding the active

region and a thin film layer patterned corresponding to the peripheral region and positioned under the spacer wall, wherein both the sealant and the spacer wall are located on the thin film layer.

- 5    Claim 17 (original): The liquid crystal on silicon display panel of claim 16 wherein the thin film layer is an anti-reflective layer.

Claim 18 (original): The liquid crystal on silicon display panel of claim 16 wherein the thin film layer is a first alignment layer.

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Claim 19 (original): The liquid crystal on silicon display panel of claim 18 further comprising:

a second alignment layer positioned on the first substrate and opposite to the first alignment layer, wherein the first alignment layer and the second alignment layer are both

15    vertical alignment layers.

Claim 20 (canceled).

Claim 21 (canceled).

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Claim 22 (previously presented): The liquid crystal display panel of claim 1 wherein the spacer wall comprises inorganic materials or photoresist materials, such as silicon dioxide or silicon nitride.

- 25    Claim 23 (previously presented): The liquid crystal display panel of claim 1 wherein the spacer block comprises inorganic materials or photoresist materials, such as silicon dioxide or silicon nitride.

Claim 24 (previously presented): The liquid crystal display panel of claim 1 wherein the spacer wall separates the liquid crystal layer from the sealant.

5 Claim 25 (previously presented): The liquid crystal display panel of claim 2 wherein a portion of the thin film layer is located on a portion of the active region and the thin film layer obstructs light so that the peripheral region and the portion of the active region are kept in a dark state.

10 Claim 26 (previously presented): The liquid crystal display panel of claim 6 wherein the spacer wall comprises inorganic materials or photoresist materials, such as silicon dioxide or silicon nitride.

15 Claim 27 (previously presented): The liquid crystal display panel of claim 6 wherein the spacer wall separates the liquid crystal layer from the sealant.

Claim 28 (previously presented): The liquid crystal display panel of claim 6 wherein the thin film layer obstructs light so that the peripheral region is kept in a dark state.

20 Claims 29-30 (canceled)

Claim 31 (previously presented): The liquid crystal on silicon display panel of claim 15 wherein the spacer wall comprises inorganic materials or photoresist materials, such as silicon dioxide or silicon nitride.

25 Claim 32 (previously presented): The liquid crystal on silicon display panel of claim 15 wherein the spacer wall separates the liquid crystal layer from the sealant.

Claim 33 (previously presented): The liquid crystal on silicon display panel of claim 16

wherein the thin film layer obstructs light so that the peripheral region is kept in a dark state.

Claim 34 (currently amended): The liquid crystal on silicon display panel of ~~claim 21~~  
5 claim 15 wherein the spacer block comprises inorganic materials or photoresist materials, such as silicon dioxide or silicon nitride.

Claim 35 (previously presented): The liquid crystal display panel of claim 5 further comprising:  
10 a third alignment layer covering the first substrate; and  
a fourth alignment layer covering the second substrate between the thin film layer and the second substrate.

Claim 36 (previously presented): The liquid crystal display panel of claim 1, wherein the  
15 spacer wall further comprises a second spacer block positioned in parallel with the spacer block.